

CLAIM AMENDMENTS

1. (Currently Amended) In a switching amplifier of the type wherein one or more references are coupled to a load through gated switches controlled by a pulse-width modulated input signal, the improvement comprising:

adding a minimum pulse width of the same polarity to a pair of switching devices, one on either side of the load, to null the common-mode output presented to the load [[,]]

~~wherein each pair of switching devices is powered by a common reference.~~

2. (Original) The improved switching amplifier of claim 1, wherein the switches are arranged as differential pairs on either side of the load.

3. (Original) The improved switching amplifier of claim 1, wherein minimum pulse width is added during the pulse-width modulation of the input signal.

4. (Currently Amended) An enhanced performance switching amplifier coupling an input signal to a load, comprising:

at least one electrically controlled switch coupled to each side of the load ~~powered by a common reference~~; and

a waveform generator operative to perform the following functions:

- a) control the switches in accordance with the input signal, and
- b) adding a minimum pulse width of the same polarity to both electrically controlled switches to null common-mode output presented to the load.

5. (Original) The improved switching amplifier of claim 4, wherein the switches are arranged as differential pairs on either side of the load.

6. (Original) The improved switching amplifier of claim 4, wherein minimum pulse width is added by the pulse-width modulator.